

**CSMSS**  
**CHHATRAPATI SHAH MAHARAJ SHIKSHAN SANSTHA's**  
**CHH. SHAH COLLEGE OF ENGINEERING**

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**REPORT OF**  
**“PARENT MEET-2025”**

**Date: 27/09/2025**

**Time: 11:00 AM**

**Venue: A-203, Seminar Hall A Building, Electronics Engineering (VLSI D & T) Department, CSMSS CSCOE.**

**Goal:** The main goal of conducting a Parent Meet is to establish effective communication and collaboration between **parents, faculty, and the institution** for the holistic development of students.

**Objective:** The objective of a parent-teacher meeting (PTM) is to establish a clear and productive communication channel between parents and teachers to enhance the child's overall development. The specific objectives can include:

**1. Assessing Academic Progress:** To provide a detailed understanding of the child's academic performance, including strengths, areas for improvement, and any academic challenges.

**2. Identifying Learning Needs:** To identify any specific learning needs, gaps, or potential for advanced learning, and to discuss strategies for addressing these needs.

**3. Parental Involvement:** To explore ways for parents to actively support their child's education, either by reinforcing learning at home or by addressing specific issues raised by the teacher.

**4. Strengthening the Parent:** Teacher Relationship: To build trust and mutual understanding between parents and teachers, ensuring that communication remains open and continuous for the benefit of the child's development.

**Benefits:** Parent-teacher meetings (PTMs) offer a variety of benefits that enhance a child's educational experience by fostering collaboration between parents and teachers. Some key benefits include:

**1. Improved Academic Performance:**

- Holistic Understanding:** Teachers gain insights from parents about the child's behavior, learning habits, and circumstances at home, while parents better understand their child's academic progress. This shared understanding helps tailor approaches that benefit the child's learning.
- Targeted Support:** Specific areas of academic concern can be identified early, allowing for personalized support and intervention.

**2. Enhanced Behavioral and Social Development:**

- Behavioral Insights:** Teachers can share observations about the child's social interactions and behavior in school, which helps parents address any concerns related to emotional or social development.
- Consistent Guidance:** Teachers and parents can collaborate to provide consistent discipline, guidance, and positive reinforcement both at home and at school.

**3. Goal Setting and Accountability:**

- Clear Objectives:** PTMs help in setting specific academic and personal development goals for the child, with both the teacher and parent working toward achieving these objectives.

- **Monitoring Progress:** Regular communication through PTMs allows for ongoing evaluation of the child's progress, enabling timely adjustments to learning strategies.

#### 4. Strengthened Parent-Teacher Relationship:

- **Mutual Understanding:** Regular communication helps build trust and understanding between parents and teachers, which is crucial for the child's long-term success.
- **Increased Parental Involvement:** Parents feel more involved and connected to their child's education, which has been shown to have a positive impact on student outcomes.

#### About Event:

- The one-day event was organized by department of Electronic Engineering (VLSI Design and Tech).
- The event started with lamp lightening ceremony by Hon. Dr. S. J. Honade, HoD EE (VLSI D & T).
- Prof. P. N. Kathar, Prof. T. A. Mohije, two parents and all faculty members of department of EE (VLSI D & T) were present for the meeting
- Dr. S. J. Honade, HoD EE (VLSI D & T) felicitate Two of the parents.
- Dr. S. J. Honade, HoD EE (VLSI D & T), address the parents about roles and responsibilities of a students in society and about importance of parent teacher meet.
- The workshop was coordinated by Prof. P. N. Kathar and Prof. G. G. Patil.
- A vote of gratitude was presented by Prof. P. N. Kathar TG TY EE (VLSI D & T).

#### Glimpses of the Event





Chhatrapati Sambhajinagar, Maharashtra, India  
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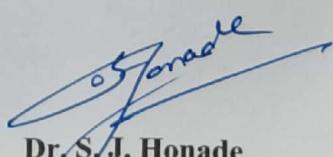


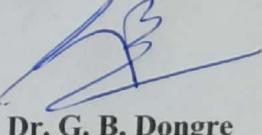
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Mr. P. N. Kathar  
Event Coordinator

  
Dr. S. J. Honade  
HOB, EE (VLSI D & T)

  
Dr. G. B. Dongre  
Principal

